

P I C 18 F 4525 の A D 変換 設 定

PCFG3:PCFG0 の 4 ビットで AN12:AN0 までを一括で設定します。そのため、個別で、AN や I/O の設定はできません。

bit 3-0 **PCFG3:PCFG0: A/D Port Configuration Control bits:**

PCFG3: PCFG0	AN12	AN11	AN10	AN9	AN8	AN7 ⁽²⁾	AN6 ⁽²⁾	AN5 ⁽²⁾	AN4	AN3	AN2	AN1	AN0
0000 ⁽¹⁾	A	A	A	A	A	A	A	A	A	A	A	A	A
0001	A	A	A	A	A	A	A	A	A	A	A	A	A
0010	A	A	A	A	A	A	A	A	A	A	A	A	A
0011	D	A	A	A	A	A	A	A	A	A	A	A	A
0100	D	D	A	A	A	A	A	A	A	A	A	A	A
0101	D	D	D	A	A	A	A	A	A	A	A	A	A
0110	D	D	D	D	A	A	A	A	A	A	A	A	A
0111 ⁽¹⁾	D	D	D	D	D	A	A	A	A	A	A	A	A
1000	D	D	D	D	D	D	A	A	A	A	A	A	A
1001	D	D	D	D	D	D	D	A	A	A	A	A	A
1010	D	D	D	D	D	D	D	D	A	A	A	A	A
1011	D	D	D	D	D	D	D	D	D	A	A	A	A
1100	D	D	D	D	D	D	D	D	D	D	A	A	A
1101	D	D	D	D	D	D	D	D	D	D	D	A	A
1110	D	D	D	D	D	D	D	D	D	D	D	D	A
1111	D	D	D	D	D	D	D	D	D	D	D	D	D

A = Analog input

D = Digital I/O

P I C 16 F 887 の A D 変換 設 定

各 AN ごとにアナログとデジタルを設定できます。

REGISTER 3-3: ANSEL: ANALOG SELECT REGISTER

R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1
ANS7 ⁽²⁾	ANS6 ⁽²⁾	ANS5 ⁽²⁾	ANS4	ANS3	ANS2	ANS1	ANS0	
bit 7							bit 0	

Legend:

R = Readable bit W = Writable bit U = Unimplemented bit, read as '0'
 -n = Value at POR '1' = Bit is set '0' = Bit is cleared x = Bit is unknown

bit 7-0 **ANS<7:0>**: Analog Select bits
 Analog select between analog or digital function on pins AN<7:0>, respectively.
 1 = Analog input. Pin is assigned as analog input⁽¹⁾.
 0 = Digital I/O. Pin is assigned to port or special function.

REGISTER 3-4: ANSELH: ANALOG SELECT HIGH REGISTER

U-0	U-0	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1
—	—	ANS13	ANS12	ANS11	ANS10	ANS9	ANS8
bit 7						bit 0	

Legend:

R = Readable bit W = Writable bit U = Unimplemented bit, read as '0'
 -n = Value at POR '1' = Bit is set '0' = Bit is cleared x = Bit is unknown

bit 7-6 **Unimplemented:** Read as '0'
 bit 5-0 **ANS<13:8>**: Analog Select bits
 Analog select between analog or digital function on pins AN<13:8>, respectively.
 1 = Analog input. Pin is assigned as analog input⁽¹⁾.
 0 = Digital I/O. Pin is assigned to port or special function.

PIC18K46K22 のAD変換設定

各ANごとにアナログとデジタルの設定が変更できます。

REGISTER 10-3: ANSELA – PORTA ANALOG SELECT REGISTER

U-0	U-0	R/W-1	U-0	R/W-1	R/W-1	R/W-1	R/W-1
—	—	ANSA5	—	ANSA3	ANSA2	ANSA1	ANSA0
bit 7							bit 0

Legend:

R = Readable bit W = Writable bit U = Unimplemented bit, read as '0'
 -n = Value at POR '1' = Bit is set '0' = Bit is cleared x = Bit is unknown

bit 7-6 **Unimplemented:** Read as '0'
 bit 5 **ANSA5:** RA5 Analog Select bit
 1 = Digital input buffer disabled
 0 = Digital input buffer enabled
 bit 4 **Unimplemented:** Read as '0'
 bit 3-0 **ANSA<3:0>:** RA<3:0> Analog Select bit
 1 = Digital input buffer disabled
 0 = Digital input buffer enabled

REGISTER 10-4: ANSELB – PORTB ANALOG SELECT REGISTER

U-0	U-0	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1
—	—	ANSB5	ANSB4	ANSB3	ANSB2	ANSB1	ANSB0
bit 7							bit 0

Legend:

R = Readable bit W = Writable bit U = Unimplemented bit, read as '0'
 -n = Value at POR '1' = Bit is set '0' = Bit is cleared x = Bit is unknown

bit 7-6 **Unimplemented:** Read as '0'
 bit 5-0 **ANSB<5:0>:** RB<5:0> Analog Select bit
 1 = Digital input buffer disabled
 0 = Digital input buffer enabled

REGISTER 10-5: ANSELC – PORTC ANALOG SELECT REGISTER

R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	R/W-1	U-0	U-0
ANSC7	ANSC6	ANSC5	ANSC4	ANSC3	ANSC2	—	—
bit 7							bit 0

Legend:

R = Readable bit W = Writable bit U = Unimplemented bit, read as '0'
 -n = Value at POR '1' = Bit is set '0' = Bit is cleared x = Bit is unknown

bit 7-2 **ANSC<7:2>:** RC<7:2> Analog Select bit
 1 = Digital input buffer disabled
 0 = Digital input buffer enabled
 bit 1-0 **Unimplemented:** Read as '0'

REGISTER 10-6: ANSELD – PORTD ANALOG SELECT REGISTER

R/W-1							
ANSD7	ANSD6	ANSD5	ANSD4	ANSD3	ANSD2	ANSD1	ANSD0
bit 7							bit 0

Legend:

R = Readable bit W = Writable bit U = Unimplemented bit, read as '0'
 -n = Value at POR '1' = Bit is set '0' = Bit is cleared x = Bit is unknown

bit 7-0 **ANSD<7:0>:** RD<7:0> Analog Select bit
 1 = Digital input buffer disabled
 0 = Digital input buffer enabled

REGISTER 10-7: ANSELE – PORTE ANALOG SELECT REGISTER

U-0	U-0	U-0	U-0	U-0	R/W-1	R/W-1	R/W-1
—	—	—	—	—	ANSE2 ⁽¹⁾	ANSE1 ⁽¹⁾	ANSE0 ⁽¹⁾
bit 7							bit 0

Legend:

R = Readable bit W = Writable bit U = Unimplemented bit, read as '0'
 -n = Value at POR '1' = Bit is set '0' = Bit is cleared x = Bit is unknown

bit 7-3 **Unimplemented:** Read as '0'
 bit 2-0 **ANSE<2:0>:** RE<2:0> Analog Select bit⁽¹⁾
 1 = Digital input buffer disabled
 0 = Digital input buffer enabled

Note 1: Available on PIC18(L)F4XK22 devices only.